

6. (Twice Amended) A power amplifier system for driving a load comprising:
a pulse width modulation power circuit creating ripple spectra;
an error amplifier and modulator circuit connected to an input of the pulse width modulation circuit;
a demodulation filter connected between said pulse width modulation power circuit and the load;

a feedback control loop coupled to said error amplifier and modulator circuit and including:

an active low-pass filter;

a first resistive voltage divider circuit coupled between the output of said demodulation filter and a first input of said low-pass filter;

a feedback demodulation filter coupled to a second input of said low-pass filter and including at least one isolated-integrator band-reject filter; and

a second resistive voltage divider circuit coupled between the output of said pulse width modulation power circuit and said feedback demodulation filter.

—9. (New) A power amplifier system for driving a load comprising:
a pulse width modulation circuit having an input and an output, said pulse width modulation circuit operable to create ripple spectra;
an error amplifier and modulator circuit connected to said input of said pulse width modulation circuit;
a demodulation filter connected to said output of said pulse width modulation circuit;
a feedback control loop coupled to said error amplifier and modulator circuit and to said output of said pulse width modulation circuit, said feedback control loop including a feedback demodulation filter, wherein an isolated-integrator frequency-rejecting network is included as part of said feedback demodulation filter.

10. (New) The system of Claim 9, wherein said isolated-integrator frequency-rejecting network is an isolated-integrator band-reject filter, including a resistor for tuning the band-reject filter.--